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AUS9-1999-0711-US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Fariborz Assaderaghi, et al.

Serial No: 09/964,127

Filed: September 26, 2001

FOR: METHOD FOR PRODUCING AN
INTEGRATED CIRCUIT CAPACITOR)

Group Art Unit: 2822

Examiner: Jeff Vockrodt

Via Facsimile: 703-746-4000

Box Issue Fee

Commissioner of Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT UNDER SECTION C.F.R. 1.312

In response to the Notice of Allowability mailed December 29, 2003, in the above-identified application, the Applicant respectfully requests the following changes to correct typographical errors in the specification.